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APPLICANTS

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** CONTINUING DATA *****

None BT

** FOREIGN APPLICATIONS *****

JAPAN 2001-118413 04/17/2001

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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <u>h h tr</u> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 25	TOTAL CLAIMS 32	INDEPENDENT CLAIMS 12
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TITLE

Fabrication method of semiconductor integrated circuit device

FILING FEE

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